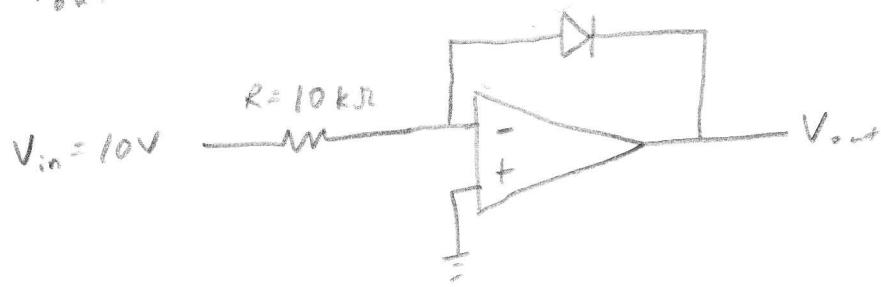


Find V_{out}



Note - This is a log amp

KCL @ V_+

$$\frac{V_{in}}{R} = I_o (e^{V_{out}/nV_T} - 1) \approx I_o e^{-V_{out}/nV_T} \text{ if forward biased}$$

$$\frac{V_{in}}{R I_o} = e^{-V_{out}/nV_T}$$

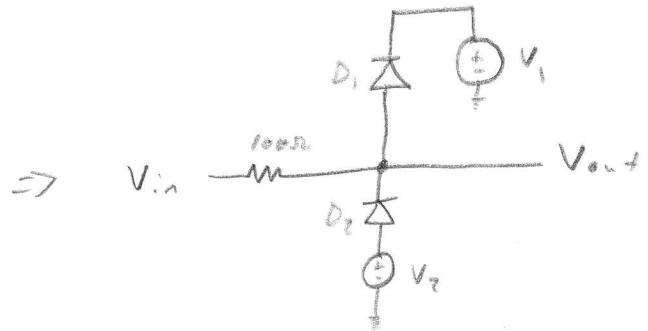
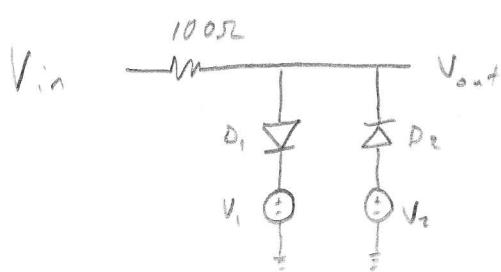
$$\ln\left(\frac{V_{in}}{R I_o}\right) = -\frac{V_{out}}{nV_T}$$

$$V_{out} = -nV_T \ln\left(\frac{V_{in}}{R I_o}\right)$$

$$n=1 \\ V_T = \frac{kT}{q} = \frac{(1.38 \times 10^{-23} \text{ J/K})(300 \text{ K})}{(1.602 \times 10^{-19} \text{ C})} = 25.84 \text{ mV}$$

T was given at the top of the homework assignment

$$V_{out} = -(25.84 \text{ mV}) \ln\left(\frac{10 \text{ V}}{(10 \text{ k}\Omega)(0.1 \mu\text{A})}\right) = \boxed{-0.5951 \text{ V}}$$



$$-1V \leq V_{out} \leq 5V$$

$$V_{on} = 0.7V$$

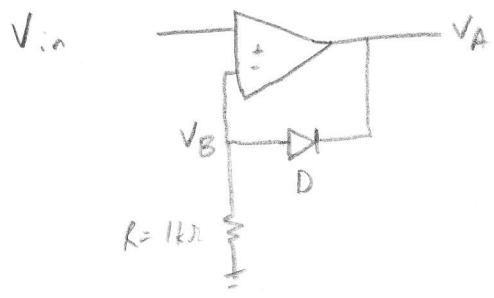
Note, the value of the resistor does not play a significant role

D_1 will forward bias if $V_{out} > V_{on} + V_1$

$$\Rightarrow V_1 = 5V - 0.7V = \underline{\underline{4.3V}}$$

D_2 will forward bias if $V_{out} < -V_{on} + V_2$

$$\Rightarrow V_2 = V_{out} + V_{on} = -1V + 0.7V = \underline{\underline{-0.3V}}$$



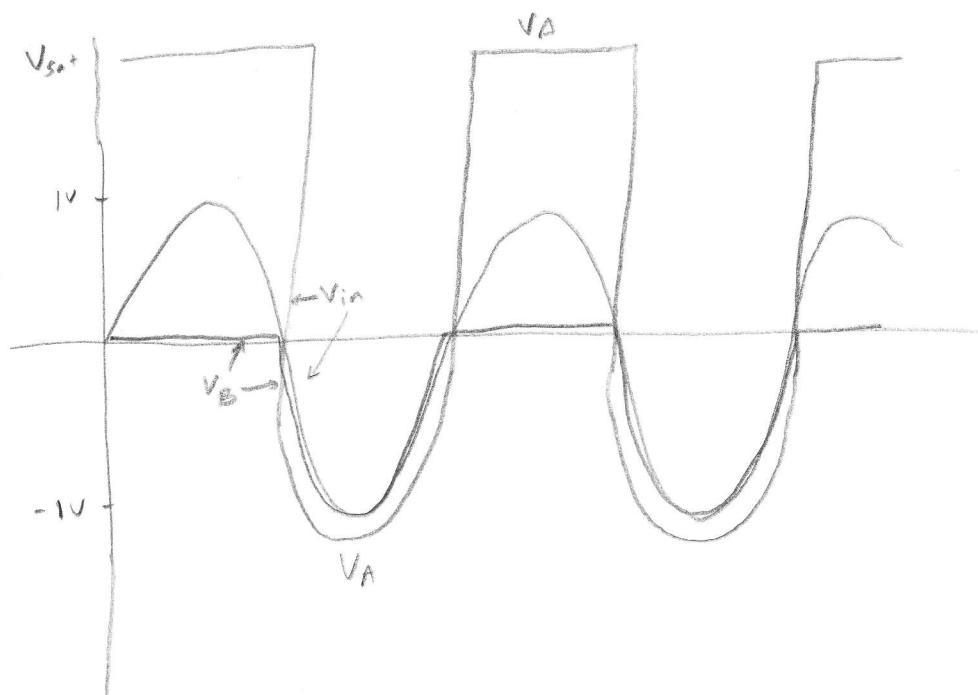
if $V_{in} > 0V \Rightarrow D = OFF$
 $\Rightarrow V_A = V_{sat}$
 $\Rightarrow V_B = 0V$

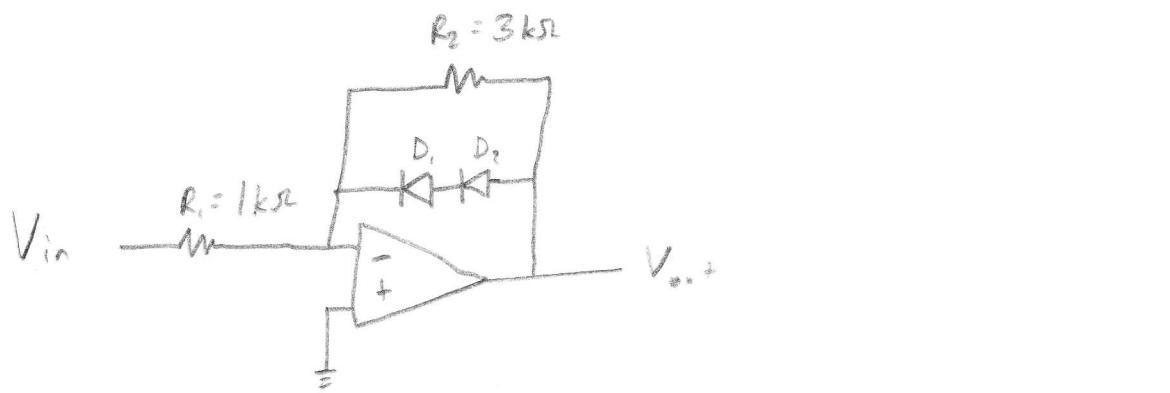
if $V_{in} < 0V \Rightarrow D = ON$
 Negative Feedback is engaged

$$V_B = V_{in}$$

$$V_A = V_B - V_{on}$$

$$V_A = V_{in} - V_{on}$$





If $V_{in} > 0 \Rightarrow D_1$ and D_2 are off
 \Rightarrow Looks like an inverting amp with gain = -3

If $V_{in} < 0 \Rightarrow$ Two cases

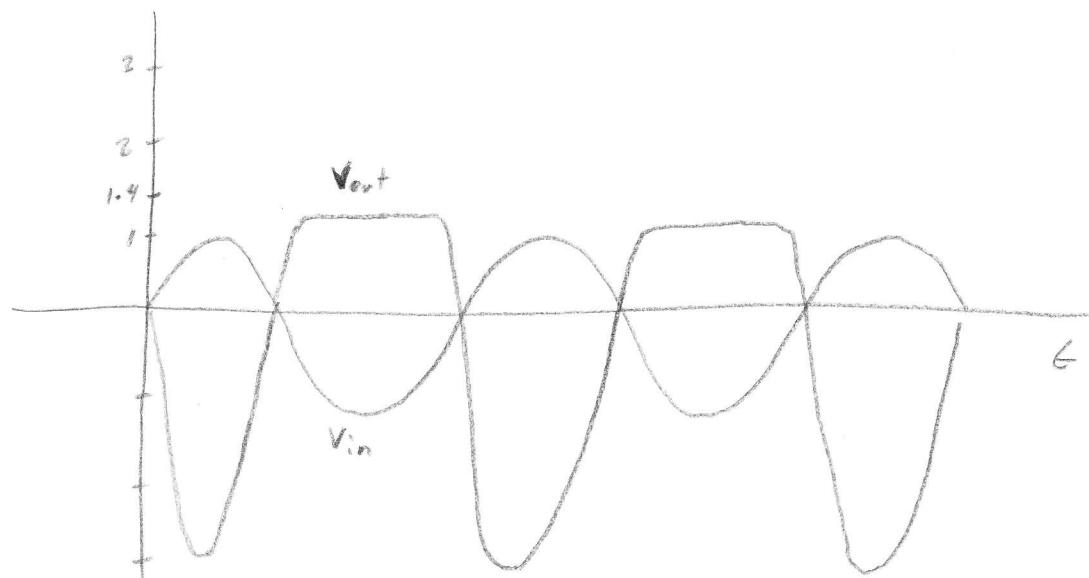
Case 1
If $0 < V_{out} < 2V_{on}$ since there are two diodes in series

D_1 & D_2 are off
Looks like an inverting amp with gain = -3
, This applies $0V > V_{in} > -0.4667V$

If $V_{out} > 2V_{on}$

D_1 & D_2 are on
 $V_{out} = 1.4V$ ($2V_{on}$)

\therefore This applies for $V_{in} < -0.4667V$





D_1

D_1 is a "regular" diode

D_2

D_2 is a Zener diode

V_{out} will be restricted to be
between

D_1 will be OFF

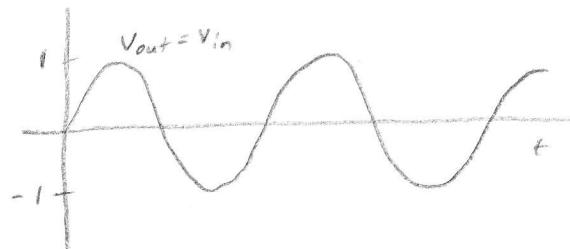
$\leftarrow V_{low} = -\infty \Omega$

D_1 will be on

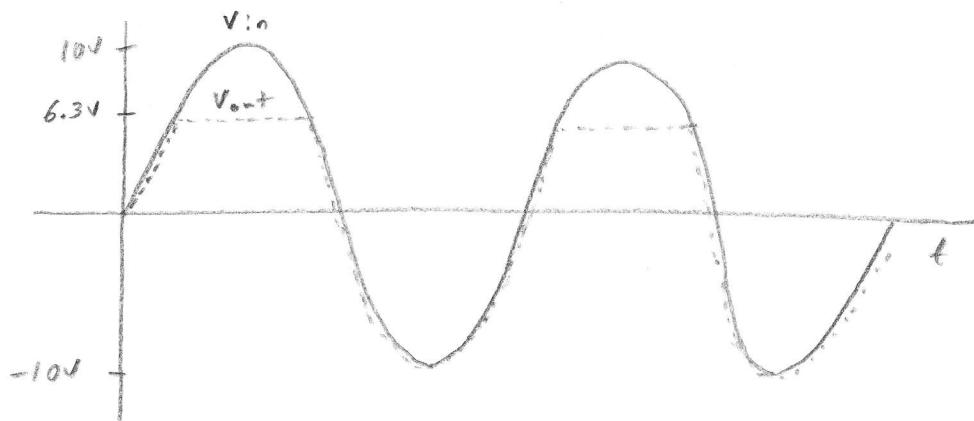
$\leftarrow V_{high} = V_{DN,1} + V_{Z,2} = 6.3V$

D_2 will be in breakdown

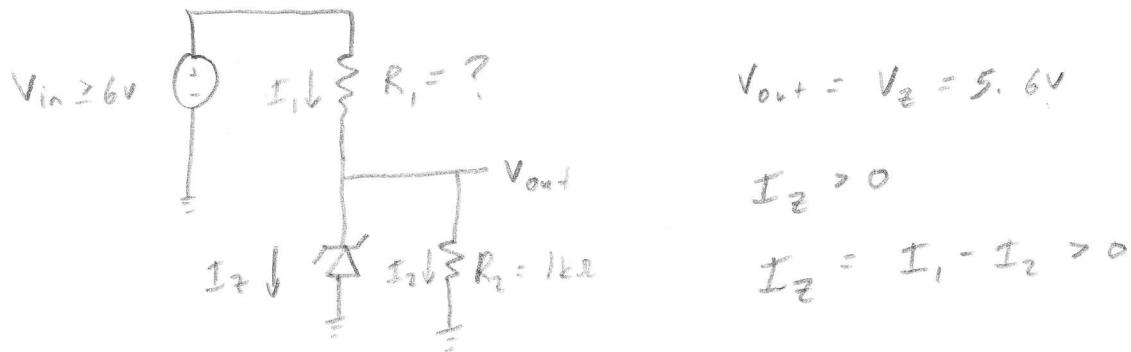
For a $2V_{pp}$ signal, $V_{out} = V_{in}$



For a $20V_{pp}$ signal



Determine the range of allowable values of R_1 for $V_{out+} \approx 5.6V$.



$$I_2 = \frac{V_{in} - V_z}{R_1} - \frac{V_z}{R_2} > 0$$

$$\frac{V_{in} - V_z}{R_1} > \frac{V_z}{R_2}$$

$$R_1 < R_2 \left(\frac{V_{in} - V_z}{V_z} \right)$$

Choose the minimum possible value of V_{in} to determine the constraint on R_1 .

$$R_1 < (1k\Omega) \left(\frac{6V - 5.6V}{5.6V} \right)$$

$$R_1 < 71.43\Omega$$