## RULES

This is a closed book, closed notes test. You are, however, allowed one piece of paper (both sides) for notes and definitions, but no sample problems. You must staple your equations sheet to the back of your test when you hand your test in.

You are permitted to use a calculator.

You have 50 minutes to complete the test. Please read through the entire test before starting, and read through the directions carefully. To receive partial credit, you must show your work.

There is to be absolutely no cheating. Cheating will not be tolerated.

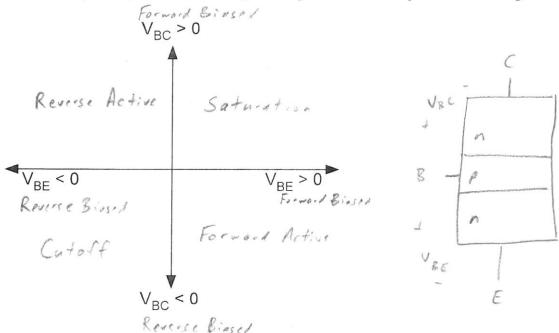
I you have any questions, please raise your hand, and I will come to you to answer them. Do not hesitate to ask questions.

$$\beta$$
 = 100,  $V_{BE,ON}$  = 0.7V,  $V_{CE,SAT}$  = 0.2V  
 $V^{+}$  = 10V,  $V^{-}$  = -10V  
 $U_{T}$  = 26mV,  $V_{A}$  = 100V

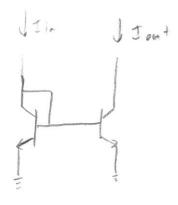
Problem	Value	Score
1	10	
2	25	
3	30	
4	35	
Total	100	

(10 Points)

A. For an npn BJT, determine the region of operation in each quadrant of the figure below.



B. Draw the schematic of a "current mirror" circuit.



(25 Points)

Determine a range of values of R such that the transistor operates in forward active.

$$V^{+}$$
 $V^{+}$ 
 $V^{+}$ 
 $V^{+}$ 
 $V^{+}$ 
 $V^{+}$ 
 $V^{+}$ 
 $V^{+}$ 
 $V^{-}$ 
 $V^{+}$ 
 $V^{+$ 

Therenin Equivalent at the Base
$$V_{BB} = V^{+} \frac{R_{2}}{R_{1}+R_{2}} + V^{-} \left(\frac{R_{1}}{R_{1}+R_{2}}\right) = 6V$$

$$R_{B} = R_{1}//R_{2} = 80 \text{ ksz}$$

To be in Formerd Active, 
$$V_{EC} > V_{EC,SAT}$$

KVL around the  $E - C$  loop

 $V^{+} = I_{E}R_{E} + V_{EC} + I_{C}R + V^{-}$ 
 $V^{+} = I_{E}R_{E} + V_{EC} + I_{C}R + V^{-}$ 
 $V_{EC} = V^{+} = V^{-} + I_{C}\left(\frac{1+\beta}{\beta}R_{E} + R\right) > V_{EC,SAT}$ 
 $V^{+} - V_{EC,SAT} - V^{-} > I_{C}\left(\frac{1+\beta}{\beta}R_{E} + R\right)$ 
 $R < \frac{V^{+} - V_{EC,SAT} - V^{-}}{I_{C}} - \frac{1+\beta}{\beta}R_{E}$ 
 $R < \frac{V^{+} - V_{EC,SAT} - V^{-}}{I_{C}} - \frac{1+\beta}{\beta}R_{E}$ 

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Problem 2 Work Page

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(30 Points)

A. Draw the small-signal equivalent model of the following circuit. If there are any resistors in parallel, then draw them as the parallel combination (e.g. draw a single resistor of value  $R_x/R_v$ ). You may assume that the transistor operates in forward active.

B. Derive an expression for the output resistance of this circuit (Rout). You must show your work to receive full credit. (Hint. This is one of the three "two-port parameters.") Do not make any simplifications to this expression – provide the complete expression for R<sub>out</sub>. Clearly mark your answer (with a box or a circle).

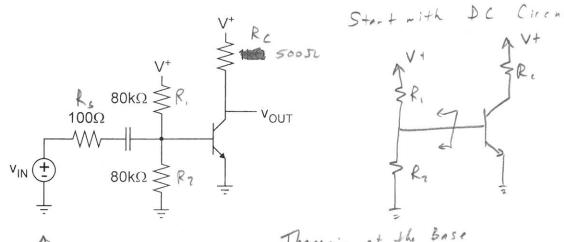
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Problem 3 Work Page

(35 Points)

Determine the small-signal voltage gain for the following circuit.



V38 RSIBY FE

KCL around the B-E loop

VBB = IBRB + VBE, ON

IB - VBB - VBE, ON - 107. 5,0A

Assuming Forward Active

Ic = & IB = 10.75 mA

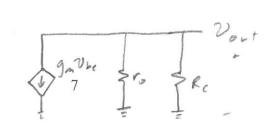
Vc = V+ - Ic Re = 5.375 V

Forward Action Operation

Small-Signal Model

Let Rin = RB //rn = 240.45

Vi. Vbc 3 Ri//R2 3 14



## Problem 4 Work Page

$$v_{be} = v_{in} \frac{R_{in}}{R_{in} + R_{s}}$$

$$v_{out} = -g_{m} v_{be} r_{o} / R_{c} = -g_{m} \left( r_{o} / R_{c} \right) \frac{R_{in}}{R_{in} + R_{s}} \left( v_{in} \right)$$

$$a_{v} = \frac{v_{out}}{v_{in}} = -g_{m} r_{o} / R_{c} \frac{R_{in}}{R_{in} + R_{s}} = \left[ -\frac{138.6}{38.6} \right]$$